COMPLEMENTARY 30V ENHANCEMENT MODE MOSFET H-BRIDGE

SUMMARY

N-Channel = $V_{(BR)DSS}$ = 30V : $R_{DS(on)}$ = 0.12 Ω ; I_D = 3.1A P-Channel = $V_{(BR)DSS}$ = -30V : $R_{DS(on)}$ = 0.21 Ω ; I_D = -2.3A

DESCRIPTION

This new generation of trench MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.

FEATURES

- · Low on-resistance
- · Fast switching speed
- Low threshold
- · Low gate drive
- Single SM-8 surface mount package

APPLICATIONS

• Single phase DC fan motor drive

ORDERING INFORMATION

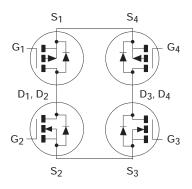
DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMHC3A01T8TA	7″	12mm	1,000 units
ZXMHC3A01T8TC	13″	12mm	4,000 units

DEVICE MARKING

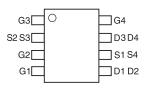
 ZXMH C3A01



SM8



PINOUT



Top View



ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	N-Channel	P-channel	UNIT
Drain-source voltage	V_{DSS}	30	-30	V
Gate-source voltage	V _{GS}	±20	±20	V
Continuous drain current (V _{GS} = 10V; T _A =25°C) ^{(b)(d)}	I _D	3.1	-2.3	А
(V _{GS} = 10V; T _A =70°C) ^{(b)(d)}		2.5	-1.8	А
$(V_{GS} = 10V; T_A = 25^{\circ}C)^{(a)(d)}$		2.7	-2.0	Α
Pulsed drain current (c)	I _{DM}	14.5	-10.8	А
Continuous source current (body diode) (b)	I _S	2.3	-2.2	А
Pulsed source current (body diode) (c)	I _{SM}	14.5	-10.8	А
Power dissipation at T _A =25°C ^{(a) (d)}	P _D	1.3		W
Linear derating factor		10.4		mW/°C
Power dissipation at T _A =25°C (b) (d)	P _D	1.	W	
Linear derating factor		13	mW/°C	
Operating and storage temperature range	T _j , T _{stg}	-55 to	+150	°C

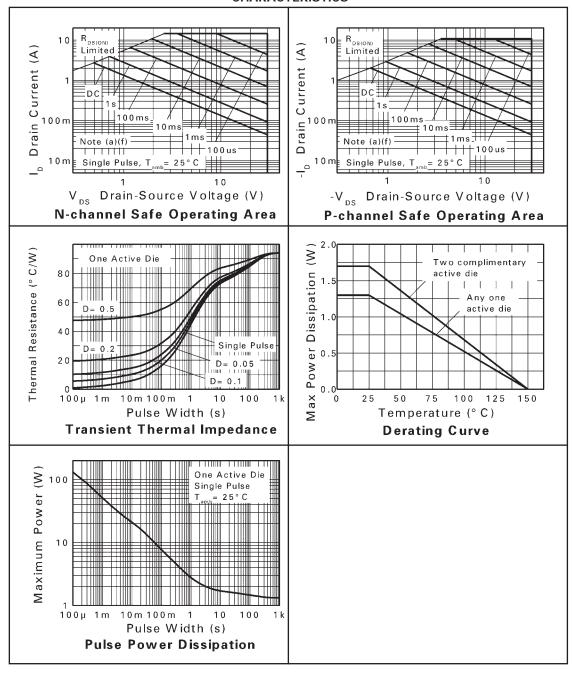
THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to ambient ^{(a) (d)}	$R_{\theta JA}$	96	°C/W
Junction to ambient (b) (d)	$R_{\theta JA}$	73	°C/W

- (a) For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.
- (b) For a device surface mounted on FR4 PCB measured at t ≤10 sec.
 (c) Repetitive rating on 50mm x 1.6mm FR4, D= 0.02, pulse width 300μS pulse width limited by maximum junction temperature. Refer to transient thermal impedance graph.
- (d) For device with one active die.



CHARACTERISTICS





N-channel

ELECTRICAL CHARACTERISTICS (at T_{amb} = 25°C unless otherwise stated)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
STATIC						
Drain-source breakdown voltage	V _{(BR)DSS}	30			V	I _D = 250μA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}			1.0	μΑ	V _{DS} =30V, V _{GS} =0V
Gate-body leakage	I _{GSS}			100	nA	V _{GS} =±20V, V _{DS} =0V
Gate-source threshold voltage	V _{GS(th)}	1.0		3.0	V	I _D = 250μA, V _{DS} =V _{GS}
Static drain-source on-state resistance ⁽¹⁾	R _{DS(on)}			0.12 0.18	Ω	V _{GS} = 10V, I _D = 2.5A V _{GS} = 4.5V, I _D = 2.0A
Forward transconductance (1) (3)	9 _{fs}		3.5		S	V _{DS} =4.5V, I _D = 2.5A
DYNAMIC (3)				•		
Input capacitance	C _{iss}		190		pF	V 25V V 0V
Output capacitance	C _{oss}		38		pF	- V _{DS} = 25V, V _{GS} =0V - f=1MHz
Reverse transfer capacitance	C _{rss}		20		pF	1 = 11011112
SWITCHING ^{(2) (3)}	•	•				
Turn-on-delay time	t _{d(on)}		1.7		ns	
Rise time	t _r		2.3		ns	V _{DD} = 15V, I _D = 2.5A
Turn-off delay time	t _{d(off)}		6.6		ns	$R_G \cong 6.0\Omega$, $V_{GS} = 10V$
Fall time	t _f		2.9		ns	
Total gate charge	Qg		3.9		nC	V _{DS} = 15V, V _{GS} = 10V
Gate-source charge	Q _{gs}		0.6		nC	I _D = 2.5A
Gate drain charge	Q _{gd}		0.9		nC	11D- 2.3A
SOURCE-DRAIN DIODE						
Diode forward voltage ⁽¹⁾	V _{SD}			0.95	V	T _j =25°C, I _S = 1.7A, V _{GS} =0V
Reverse recovery time ⁽³⁾	t _{rr}		17.7		ns	T _j =25°C, I _S = 2.5A,
Reverse recovery charge ⁽³⁾	Q _{rr}		13.0		nC	di/dt=100A/μs

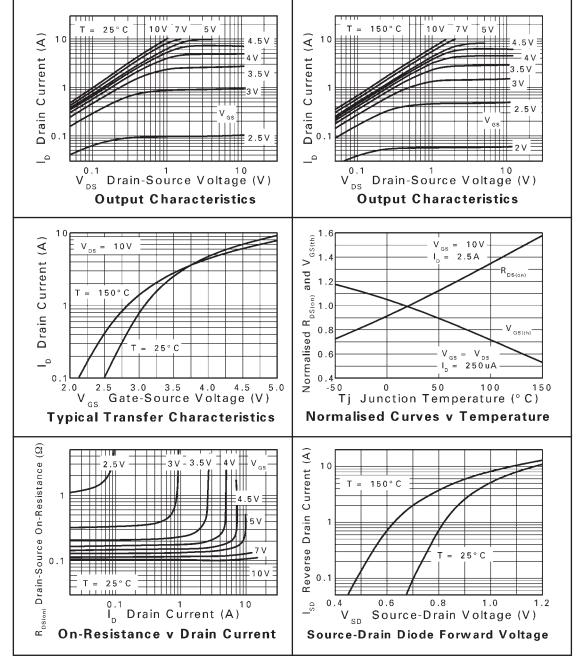
NOTES

- (1) Measured under pulsed conditions. Pulse width $\leq 300 \mu s;$ duty cycle $\leq 2\%.$
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.



N-channel

TYPICAL CHARACTERISTICS



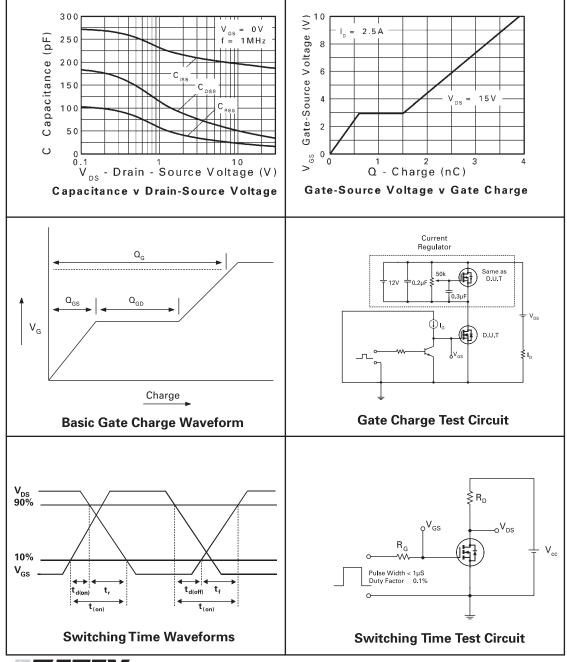
DRAFT ISSUE E - APRIL 2004

5



N-channel

TYPICAL CHARACTERISTICS





DRAFT ISSUE E - APRIL 2004

P-channel

ELECTRICAL CHARACTERISTICS (at T_{amb} = 25°C unless otherwise stated)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
STATIC						•
Drain-source breakdown voltage	V _{(BR)DSS}	-30			V	I _D = -250μA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}			-1.0	μΑ	V _{DS} = -30V, V _{GS} =0V
Gate-body leakage	I _{GSS}			100	nA	V _{GS} =±20V, V _{DS} =0V
Gate-source threshold voltage	V _{GS(th)}	-1.0		-3.0	V	I _D = -250μA, V _{DS} =V _{GS}
Static drain-source on-state resistance ⁽¹⁾	R _{DS(on)}			0.21	Ω	V _{GS} = -10V, I _D = -1.4A V _{GS} = -4.5V, I _D = -1.1A
Forward transconductance (1) (3)	g _{fs}		2.5		S	$V_{DS} = -15V, I_{D} = -1.4A$
DYNAMIC (3)						
Input capacitance	C _{iss}		204		pF	V _{DS} = -15V, V _{GS} =0V
Output capacitance	C _{oss}		39.8		pF	f=1MHz
Reverse transfer capacitance	C _{rss}		25.8		pF	
SWITCHING ^{(2) (3)}	<u>, </u>	•				,
Turn-on-delay time	t _{d(on)}		1.2		ns	V _{DD} = -15V, I _D = -1A
Rise time	t _r		2.3		ns	$R_G \cong 6.0\Omega$, $V_{GS} = -10V$
Turn-off delay time	t _{d(off)}		12.1		ns	
Fall time	t _f		7.5		ns	
Total gate charge			2.6		nC	V _{DS} = -15V, V _{GS} = -5V I _D = -1.4A
Total gate charge	Qg		5.2		nC	V _{DS} = -15V, V _{GS} = -10V
Gate-source charge	Q _{gs}		0.7		nC	I _D = -1.4A
Gate drain charge	Q _{gd}		0.9		nC	
SOURCE-DRAIN DIODE	, ,	'				
Diode forward voltage ⁽¹⁾	V _{SD}		-0.85	-0.95	V	T _j =25°C, I _S = -1.1A, V _{GS} =0V
Reverse recovery time ⁽³⁾	t _{rr}		19		ns	T _j =25°C, I _S = -0.95A,
Reverse recovery charge ⁽³⁾	Q _{rr}		15		nC	di/dt=100A/μs

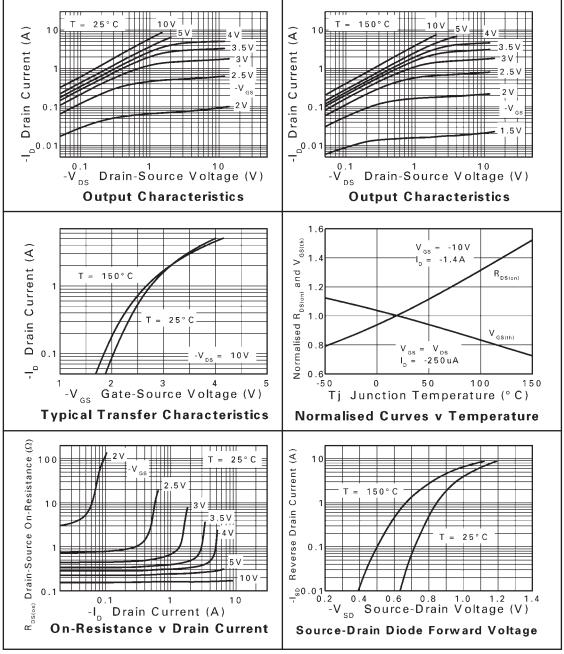
NOTES

- (1) Measured under pulsed conditions. Pulse width $\leq 300 \mu s;$ duty cycle $\leq 2\%.$
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.



P-channel

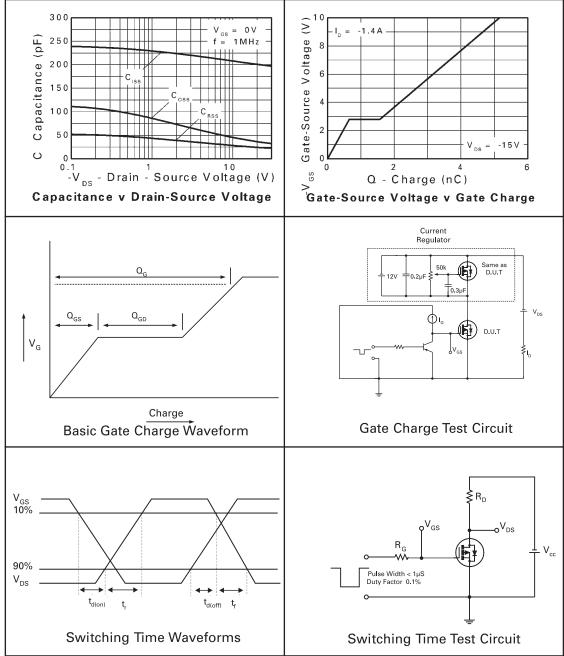






P-channel

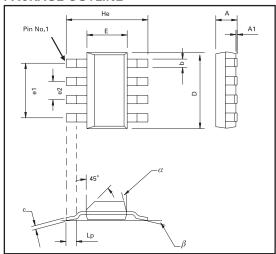
TYPICAL CHARACTERISTICS



DRAFT ISSUE E - APRIL 2004



PACKAGE OUTLINE



Controlling dimensions are in millimeters. Approximate conversions are given in inches

PACKAGE DIMENSIONS

DIM	Millimeters		Inches		DIM -	IV	lillimete	ers		Inches	;		
DIIVI	Min	Max	Тур.	Min	Max	Тур.	DIIVI	Min	Max	Тур.	Min	Max	Тур.
Α	-	1.7	-	-	0.067	-	e1	-	-	4.59	-	-	0.1807
A1	0.02	0.1	-	0.008	0.004	-	e2	-	-	1.53	-	-	0.0602
b	-	-	0.7	-	-	0.0275	Не	6.7	7.3	-	0.264	0.287	-
С	0.24	0.32	-	0.009	0.013	-	Lp	0.9	-	-	0.035	-	-
D	6.3	6.7	-	0.248	0.264	-	α	-	15°	-	-	15°	-
Е	3.3	3.7	-	0.130	0.145	-	β	-	-	10°	-	-	10°

© Zetex Semiconductors plc 2004

Europe	Americas	Asia Pacific	Corporate Headquarters
Zetex GmbH	Zetex Inc	Zetex (Asia) Ltd	Zetex plc
Streitfeldstraße 19	700 Veterans Memorial Hwy	3701-04 Metroplaza Tower 1	Lansdowne Road, Chadderton
D-81673 München	Hauppauge, NY 11788	Hing Fong Road, Kwai Fong	Oldham, OL9 9TY
Germany	USA	Hong Kong	United Kingdom
Telefon: (49) 89 45 49 49 0	Telephone: (1) 631 360 2222	Telephone: (852) 26100 611	Telephone (44) 161 622 4444
Fax: (49) 89 45 49 49	Fax: (1) 631 360 8222	Fax: (852) 24250 494	Fax: (44) 161 622 4446
europe.sales@zetex.com	usa.sales@zetex.com	asia.sales@zetex.com	hq@zetex.com

These offices are supported by agents and distributors in major countries world-wide.

This publication is issued to provide outline information only which (unless agreed by the Company in writing) may not be used, applied or reproduced for any purpose or form part of any order or contract or be regarded as a representation relating to the products or services concerned. The Company reserves the right to alter without notice the specification, design, price or conditions of supply of any product or service.

For the latest product information, log on to www.zetex.com

